TECNIA INSTITUTE OF ADVANCED STUDIES

NAAC GRADE "A" INSTITUTE (CYCLE-2)
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INSTITUTIONAL AREA, MADHUBAN CHOWK, ROHINI, DELHI-110085

DIGITAL COMPUTING CLUB	
Report (Workshop on Digital Logic Design)	
Event	Awareness
Type of Activity:	Workshop
OBE Values	Technical Competence
Mulya Pravah	Dedication
Global Impact-UNSDG No.	SDG 4 – Quality Education
Objectives	1. To develop fundamental knowledge of digital logic concepts, Boolean algebra, and logic gates. 2. To understand the design and analysis of combinational and sequential logic circuits. 3. To apply logic minimisation techniques such as Karnaugh Maps and Boolean simplification. 4. To improve problem-solving and logical reasoning skills through hardware logic implementation. 5. To gain practical experience in designing digital building blocks like adders, multiplexers, and counters.
Course Mapping	Introduction to Digital Logic Design (BCA201T, BCA207T)
Course Outcome (CO)	Students will be able to design, analyse, and optimise digital logic circuits using fundamental logic gates and minimisation techniques, applying these concepts to implement practical hardware systems.
BT Level	BTL 3 - Apply and BTL 4 - Analyse
Expected Outcomes	1. Students gain practical knowledge of digital logic principles, gate-level operations, and Boolean algebra. 2. Students can analyze, simplify, and construct combinational logic circuits. 3. Students can design basic sequential systems such as flip-flops, registers, and counters. 4. Students develop problem-solving and logical thinking skills applicable to digital system architecture.
In collaboration/association with	Domain Club: Digital Computing Club (DICT)

Name & Designation details of Resource Person- Internal Expert (LinkedIn Profile Link)	Ms. Namita Tiwari (Assistant Professor) - TIAS
Name & Designation details of Resource Person- External Expert Trained counseling expert(LinkedIn Profile Link)/	NA
Duration (Date & Time)	29-Aug-2026
Mode (Offline/Hybrid)	Offline
Venue	TIAS
No. of Internal Participants	30
No. of External Participants *[write NA if not applicable]*	0
No. of Institutions Participated	01
University Representation	NA
Awards/Recognition (if any) (Attach e-copy of Awards/Certificates)	E-Certificate

Innovation / Best Practice

The workshop adopted an interactive and concept-driven learning approach by providing students with practical exposure to Digital Logic Design concepts and digital circuit implementation. Key innovative features include:

- Integration of fundamental digital logic concepts such as logic gates, Boolean algebra, and Boolean expressions, reflecting the principles of digital system design.
- Exploration of combinational and sequential logic circuits to enhance students' analytical and logical thinking skills.
- Practical exposure to logic minimisation techniques, including Karnaugh Maps (K-Maps) and Boolean simplification.
- Application of digital logic concepts to understand and design basic digital building blocks such as adders, multiplexers, flip-flops, and counters.
- Alignment of the activity with OBE – Technical Competence, Mulya Pravah – Dedication, and UNSDG 4 – Quality Education.

Feedback:

Mode of feedback collection (form / oral / reflection / survey)	**Evidence to be maintained by the Event Coordinator:** • Attendance sheet • Event poster/flyer • Screenshots/photographs of the webinar • Feedback form and responses
Key feedback highlights	Students found the session highly practical and informative, gaining a clear understanding of digital circuit design principles, logic gate implementations, Boolean optimisation, and practical application demonstrations.
Suggestions for improvement (if any)	**NA**

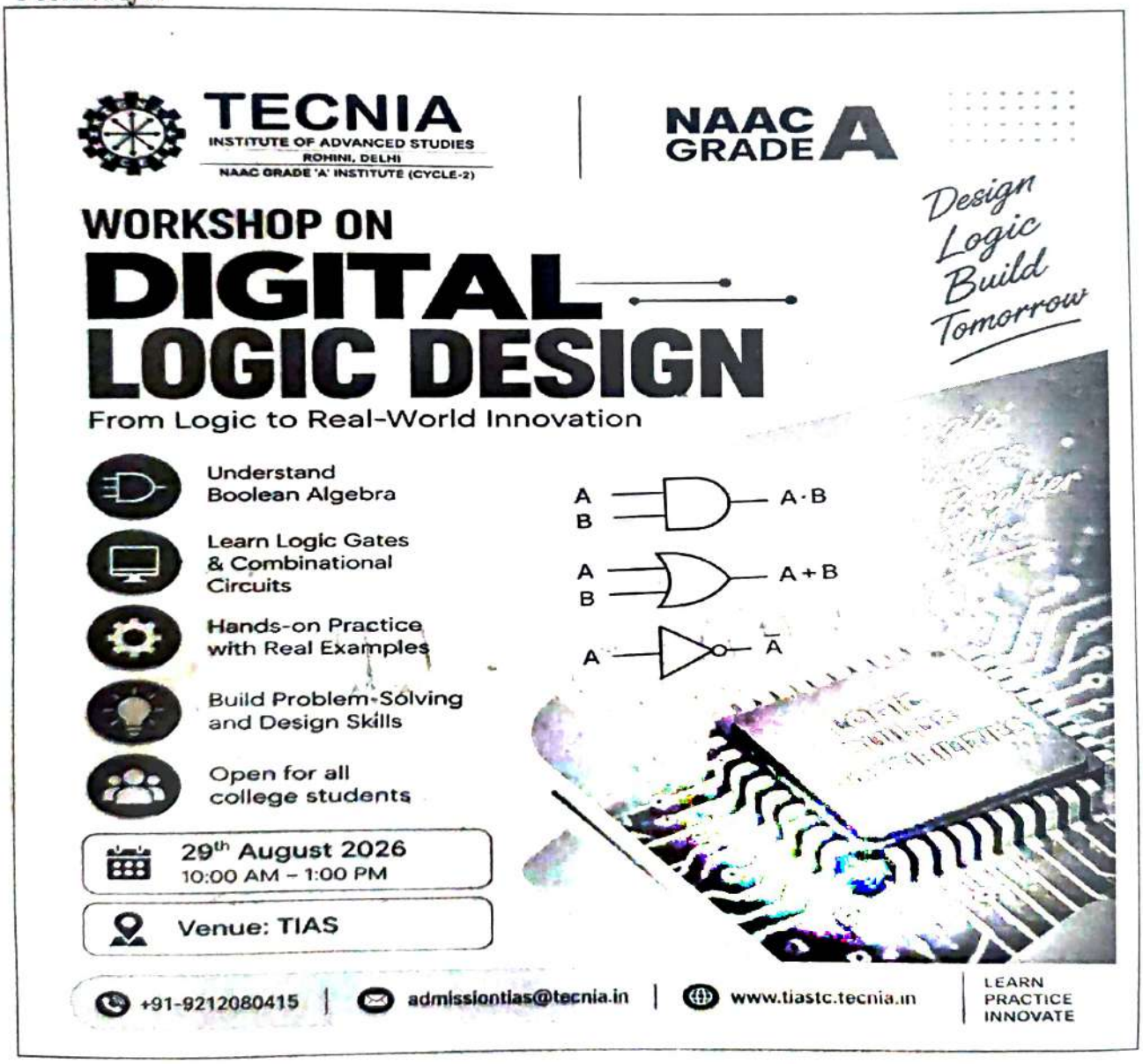
Conclude remarks (s):

The Digital Logic Design workshop successfully equipped students with practical circuit design and logic minimization skills, fulfilling the target outcomes for CO2 and BTL-3/BTL-4 applications under SDG 4 (Quality Education). Through hands-on circuit demonstrations and schematic synthesis, participants gained actionable knowledge in hardware logic design and digital system optimization. To build on this success, future sessions will incorporate additional simulation software practice and physical breadboard hardware testing, ensuring students continue to develop strong technical competence and engineering problem-solving capabilities.

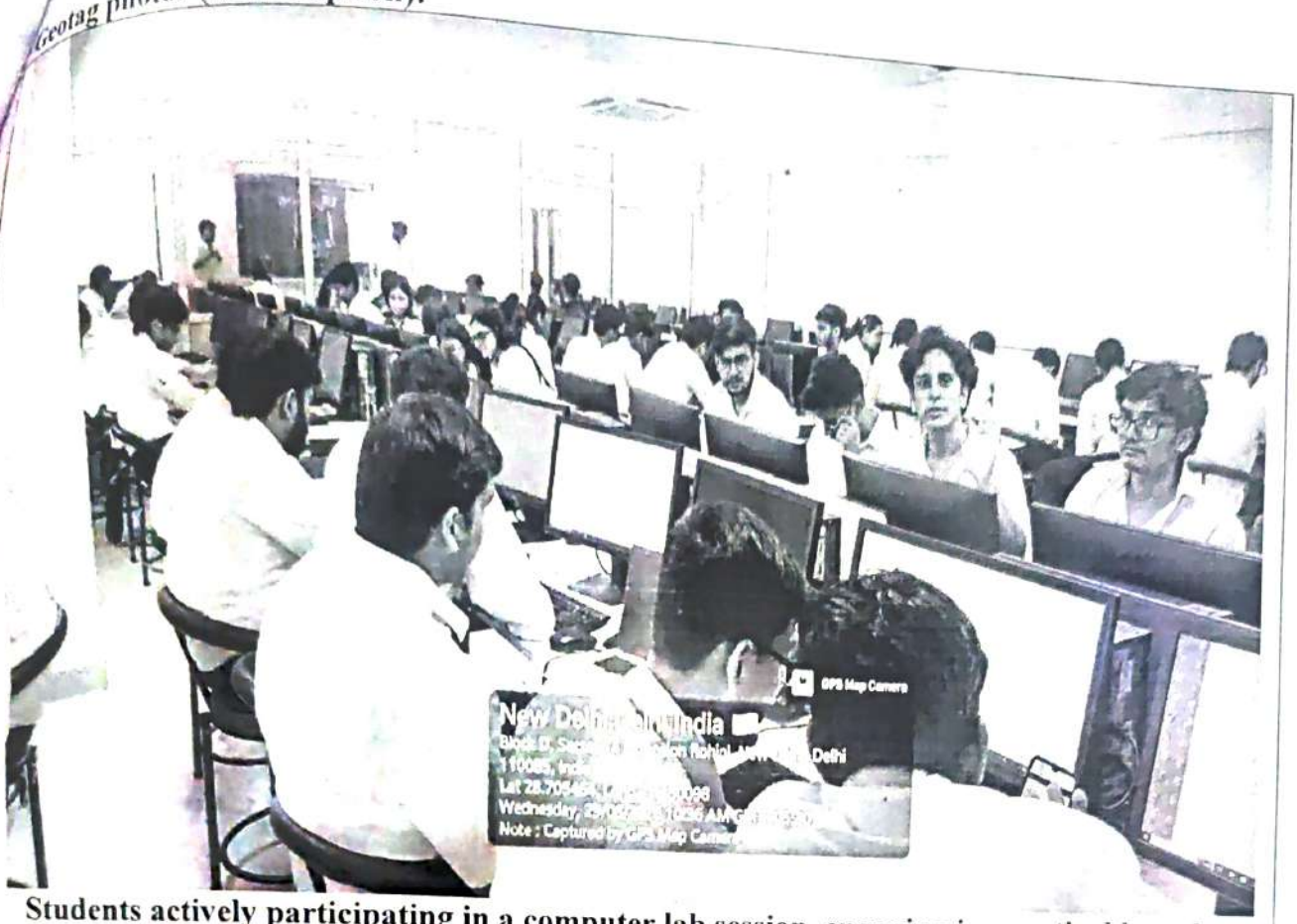
Media Coverage:

Social media link(s)	1.https://www.instagram.com/p/DcVVQyQgYkt/?igsi=OHpqbTB5OHE3d2o3
Print Media evidence (If any)	

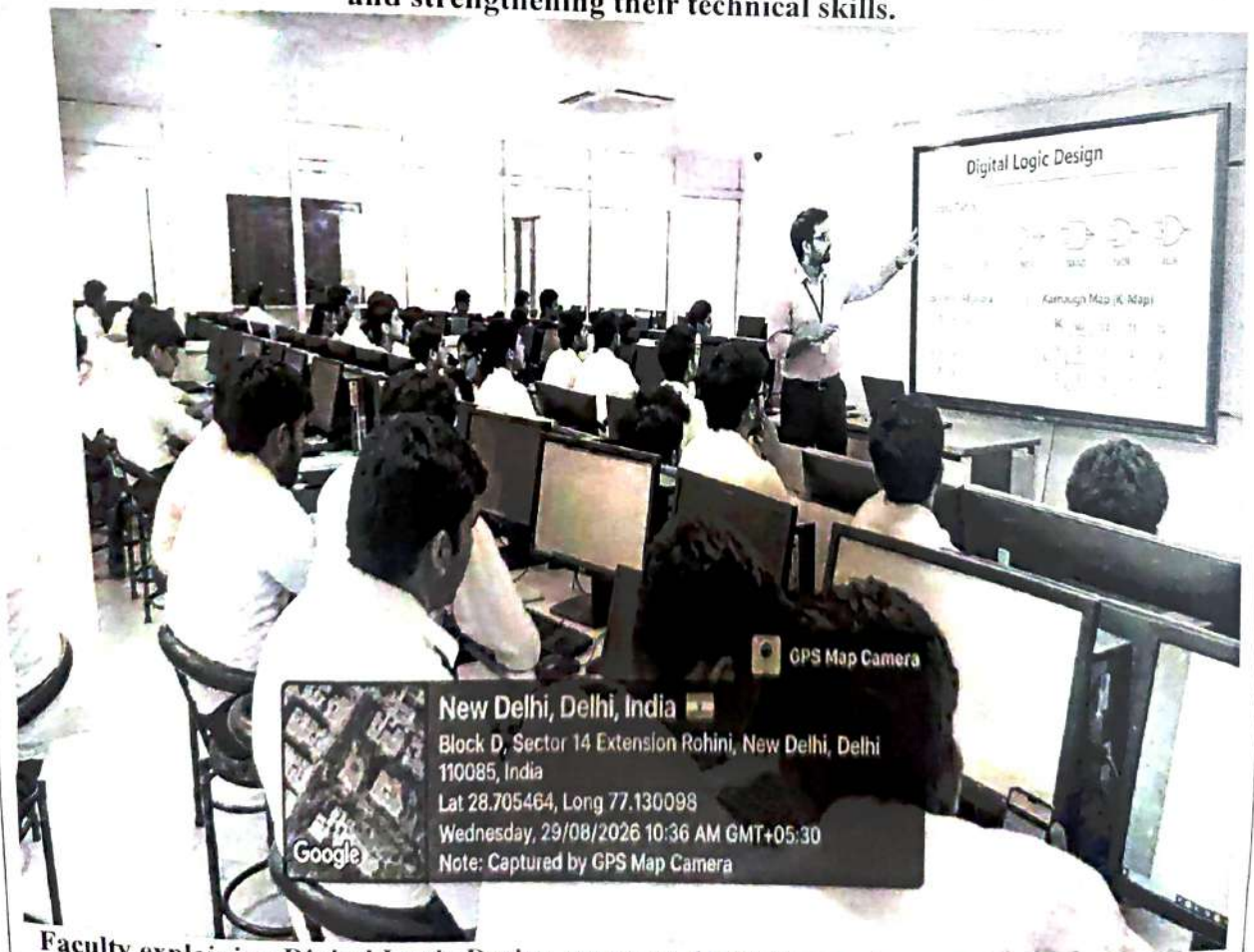
Poster/Flyer



Geotag photos (with Caption):



Students actively participating in a computer lab session, engaging in practical learning and strengthening their technical skills.



Faculty explaining Digital Logic Design concepts, including logic gates, Boolean algebra, and Karnaugh maps, to students in the computer laboratory.

of Participants (Attendance Sheet)

. No.	Enrolment No.	Name of the Student	Prog & Year	Sign
1	11017002025	Amaan Ahmed	MCA,2nd	
2	11417002025	Vipan Kumar Vansh	BCA,2nd	
3	05517002024	Krishna Chadha	BCA,2nd	
4	10221302024	Harsh Chauhan	BCA,2nd	
5	04017002026	Neha	BCA ,1st	
6	05317002026	Pratik	BCA,3rd	
7	03921302024	Dhaanya Sethi	BCA,2nd	
8	05817002026	Prachi Rawat	BCA,3rd	
9	35921302024	PADMAVATI K	BCA,3rd	
10	05321302026	Soumya Tiwari	BCA,3rd	
11	09617002025	Tanishka Gupta	BCA,2nd	
12	05817002026	Akshat Khandelwal	BCA,1st	
13	00621302025	Aayushi Gupta	BCA,2nd	
14	02517002024	Ashmit Nanda	BCA,2nd	
15	09517002024	Haya Akhtar	BCA,2nd	
16	00011702024	Dheirya Saini	BCA,2nd	
17	09121302025	Pratham Chaudhary	BCA,3rd	
18	06317002025	Lakshika	BCA,2nd	
19	05121302024	Parv m=Mehndiratta	BCA,2nd	
20	02521302025	Tanishk Gupta	BCA,3rd	
21	00321302025	Dhruv Ramesh Iyer	BCA,1st	
22	02521302024	Ali Hashim	BCA,2nd	
23	02621302024	Aman Baid	BCA,2nd	
24	00721302025	Lakshay Sharma	BCA,2nd	
25	09517002024	Lakshay Kumar	BCA,2nd	
26	08217002024	Ujjwal Chaurasia	BCA,2nd	
27	05321302025	Prince	BCA,2nd	
28	02221302025	Shreya Jain	MCA,2nd	
29	02517002025	Nishika	BCA,2nd	
30	09221302026	Abhinav	BCA,3rd	

Event Coordinator/Club Incharge

Ms. Namita Tiwari
Digital Computing Club Incharge

Dr. Ratandeep Kaur
Domain Club Head

Dr. Sushma Bahuguna
Asso. Dean, DRCP

Asso. Dean, Department of Distance & Lifelong Learning Studies
Technical Campus